

Claims

1- 11. (Cancelled).

12. (Previously presented) A method for operating a data processing device while using compressed data, comprising:

loading a non-compressed boot program from a first data memory into a first volatile working memory;
executing said boot program;
copying, initiated by said boot program, of a compressed application program from a second data memory into a second volatile working memory with simultaneous decompression of said application program, and
starting said application program through said boot program.

13. (Previously presented) The method of claim 12, wherein said loading of said boot program is controlled by a start process control device, which is separate from a processor device of said data processing device from a structural and/or functional point of view.

14. (Previously presented) The method of claim 12, wherein a first non-volatile memory of said data processing device is used as said data memory of said boot program, and a second non-volatile memory of said data processing device is used as said data memory of said application program.

15. (Previously presented) The method of claim 12, wherein said first data memory and/or said second data memory is accessed via an interface device of said data processing device.

16. (Previously presented) The method of claim 15, wherein a non-volatile memory of said data processing device is used as said first data memory of said boot program, and said application program is copied via said interface device of said data processing device from said second data memory.

17. (Previously presented) The method of claim 16, wherein, within a framework of decompression of said application program, decompression information for defined segments of said application program is read, and parameters of said decompression for each segment are adjusted based upon the appropriate decompression information.

18. (Previously presented) The method of claim 12, further comprising locating said data processing device on a space vehicle.

19. (Previously presented) The method of claim 12, further comprising locating said data processing device in a satellite navigation receiver device.

20. (Previously presented) The method of claim 12, further comprising locating said data processing device in a satellite navigation receiver device of a space vehicle.

21. (Previously presented) In a system for processing data comprising a computer-readable memory for storing data for access by a boot program comprising:

a data structure stored in said computer-readable memory, said data structure including information used by said boot program and including:

a plurality of data memory fields for storing an application program in compressed form; and

a plurality of volatile working memory fields for receiving a copy of said application program in uncompressed form;

wherein said boot program starts said application program.

22. (Previously presented) The system of claim 21 further comprising a machine-readable program carrier, wherein said boot program is stored as electronically readable control signals on said machine-readable program carrier.

23. (Currently amended) An apparatus for data processing while using compressed data comprising:

a first data memory;

a first volatile working memory;
a second data memory; and
a second volatile working memory;
wherein said first data memory is used to store a non-compressed boot program and said first volatile working memory being used to hold a copy of said boot program;

wherein said second data memory is used to store an application program in compressed form and said second volatile working memory is used to store said application program in uncompressed form;

wherein said boot program is used to take said application program in compressed form from said second data memory, to convert said application program in compressed form to uncompressed form, and to copy said application program in uncompressed form to said second volatile memory; and

wherein said boot program is used to run said application program.

24.-26. (Cancelled).

27. (Currently amended) The apparatus of claim 23 26 further comprising a start process control device, wherein said start process control device controls loading of said boot program.

28. (Previously presented) The apparatus of claim 27, further comprising a processor device of said apparatus, wherein said start process control device is separate from said processor device of said apparatus.

29. (Previously presented) The apparatus of claim 28 further comprising an interface device.

30. (Previously presented) The apparatus of claim 29, wherein said interface device accesses said first data memory and/or said second data memory.